

Design Optimization of High-Performance Electronic Devices

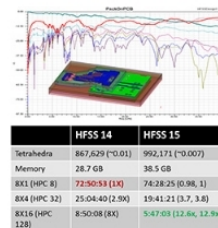
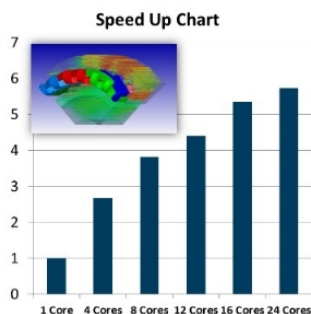
Introduction

ANSYS Optimetrics is a versatile software program that adds parametric, optimization, sensitivity and statistical analysis capabilities to ANSYS HFSS, ANSYS Maxwell and ANSYS Q3D Extractor. Optimetrics automates the design optimization process for high-performance electronic devices by quickly identifying optimal values for design parameters that satisfy user-specified constraints.

ANSYS high performance computing enables parallel processing for solution of the toughest and most challenging models – models with greater geometric detail, larger systems and more complex physics. Using high performance computing to understand detailed product behavior gives you confidence in the design and helps to ensure that your product will be delivered first and succeed in the marketplace.

Parallel processing of HFSS, SIwave, Q3D Extractor or Maxwell opens a world of bigger, faster and higher fidelity simulations. ANSYS goes well beyond simple hardware acceleration to deliver groundbreaking numerical solvers and high performance computing methodologies optimized for single multi-core machines and scalable to take advantage of full cluster power.

Join us for the workshop to learn more on how to Optimize the design of High-Performance Electronic Devices



Spectral decomposition technology in ANSYS Electronics HPC provides more efficient interpolation sweeps for USB electronic package and PCB.

Workshop Dates for September & October 2017

Date	Time	Registration
22-Sep-2017	9am to 12pm	Click Here
27-Oct-2017	9am to 12pm	Click Here

Venue

CAD-IT Consultants (Asia) Pte Ltd,
159 Sin Ming Road #03-05 Amtech Building,
Singapore 575625

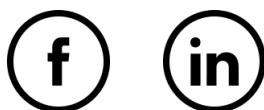
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Trainer Profile



Dr. Zheng Boyu joined CAD-IT in 2013, after working as team leader and R&D engineer in various organisations such as Institute of Microelectronics (A*STAR Singapore), United Test and Assembly Centre, EPCOS, and Huawei. He holds a Ph.D. from Nanyang Technological University, Singapore.

Dr. Zheng has a strong background in RF/Microwave, wireless, and microelectronics with more than 14 years' experience in the research and development of RF circuits, antennas, and high-speed systems including design and electrical modeling, optimisation, prototype development and testing. He has also 6 years' experience in microelectronic industry focusing on advanced packaging development. Dr. Zheng is now a Lead Technical Consultant for ANSYS's electronics business unit in the region.



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