



# Achieving an Optimum Heat Sink Design with ANSYS

**Friday, 31st March 2017 | 2:00 pm - 5:00 pm**

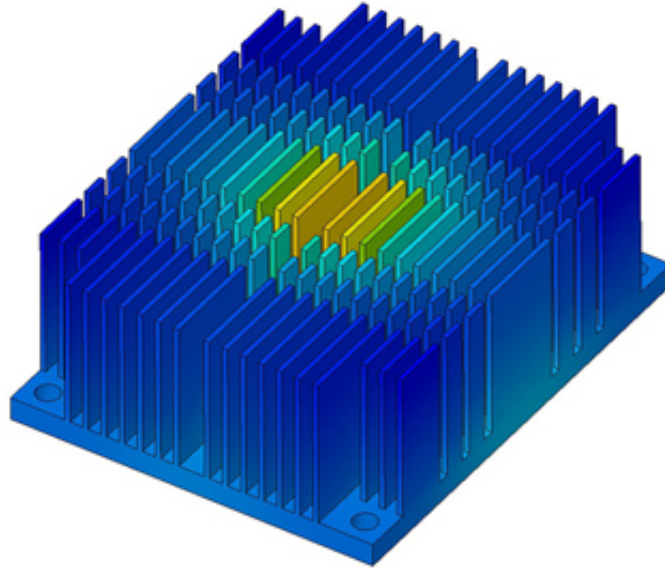
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## **Introduction**

CPUs are critical components of electronic devices. As operating power and speed increases, this leads to higher heat density and increased heat dissipation, making the CPU temperature rise. Heat sinks for CPUs are necessary to avoid the degraded performance and possible damage that comes with CPU warming. Determining a good design for heat sinks is critical to ensure that heat is effectively dissipated. Equally vital is taking into account the cost of the materials when optimizing heat sink performance.



Join us at this half-day workshop to get hands-on introduction to heat sink simulation and heat runaway design methodologies. Attendees will learn how to determine the optimal design by calculating a performance factor using the cost of materials and the CPU's temperature.

Learn how upfront simulation gives you the information to make informed decisions, reduce costly physical prototypes, and avoid unworkable designs down the road.

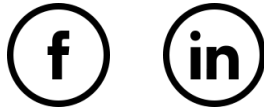
#### Presenter Profile



**Dr. Lee Yong Jiun** is a Technical Manager with CAD-IT Consultants, Singapore. He graduated from National University of Singapore with a PhD in Mechanical Engineering. His research focussed on Thermal management in Electronics, primarily using ANSYS solutions.

Dr. Lee was with the Institute of Microelectronics and served as a Project Leader to design, fabricate and characterize a package level thermal management solution. He has also worked for INTEL Technology as Assembly Technology Development Engineer, where his primary responsibility was to develop a Robust Flip Chip Assembly Process for Intel next generation chipsets. Dr Lee is responsible for the technical support of ANSYS CFD solutions.

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