

Workshop on Speed Up Dirty Geometry Preparation with FLUENT Meshing

10th October, 2016 | 2.00pm to 5.00pm

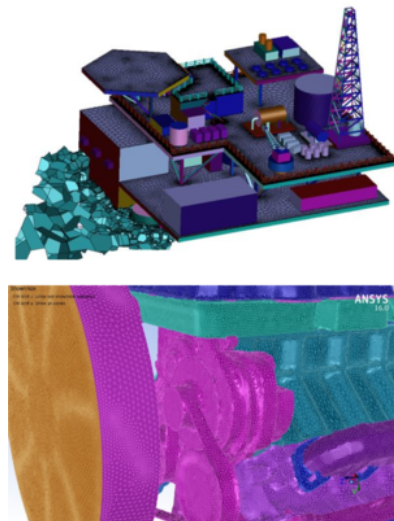
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Introduction

Accuracy of simulations to a large extent depends on better geometry definition and mesh quality. Preprocessing can form a major part of the man hours in simulation, especially for complex geometries that requires major clean-up. Certain geometries are too dirty and clean-up is almost impossible. In this seminar/workshop, wrapping concept will be introduced. This approach can be likened to shrink-wrapping luggage for external wrapping or mold filling/internal balloon inflation for internal wrapping. The wrapping process allows user to quickly overcome dirty geometries and create a viable mesh for simulation. Wrapping has several favorable features:



- Automated handling of gaps and overlaps
- Feature suppression
- Removes need for manual geometry-based closure of the geometry
- No need to have geometry in form of a watertight region - just loosely

closed to a tolerance bigger than the min size in the mesh

Join us for the workshop to learn how you can Speed Up Dirty Geometry Preparation with Fluent Meshing.

Presenter Profile:



Dr. Lee Yong Jiun is a Technical Applications Engineer at CAD-IT. He graduated from National University of Singapore with a PhD in Mechanical Engineering. His Research Focus was on Thermal management in Electronics, primarily using ANSYS solutions. Yong Jiun was with the Institute of Microelectronics and served as Project Leader to design, fabricate and characterize a package level thermal management solution. He has also worked for INTEL Technology as Assembly Technology Development Engineer, where his primary responsibility of the job was to develop a robust flip chip assembly process for Intel next generation chipsets. Yong Jiun is responsible for the technical support of ANSYS CFD solutions.

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