

# Workshop on Electric Parasitics Extraction with ANSYS Q3D

3rd October, 2016 | 2.00pm to 5.00pm

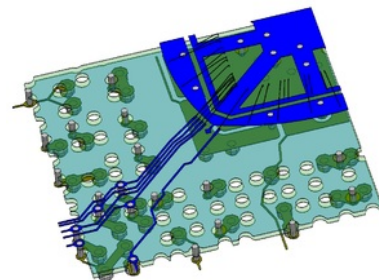
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## Introduction

The ability to generate highly accurate reduced-order SPICE models for use in circuit simulation makes ANSYS Q3D Extractor the ideal software to create IBIS package models. Engineers can study crosstalk, ground bounce, interconnect delays and ringing; this helps them to understand the performance of high-speed electronic designs, such as multilayer printed circuit boards, advanced electronic packages and 3-D on-chip passive components. In addition, Q3D Extractor is essential for extracting accurate electrical parasitics of critical interconnect components in the package (bondwires), on the board (critical nets), and for the connection path between the chip, package and board (i.e., connectors, cables, sockets and transmission lines).



Join us for the workshop to learn more about Electric Parasitics Extraction with ANSYS Q3D.

## Presenter Profile:



Dr. Zheng Boyu joined CAD-IT in 2013, after working as team leader and R&D engineer in various organisations such as Institute of Microelectronics (A\*STAR Singapore), United Test and Assembly Centre, EPCOS, and Huawei. He holds a Ph.D. from Nanyang Technological University, Singapore.

Dr. Boyu has a strong background in RF/Microwave, wireless, and microelectronics with more than 14 years' experience in the research and development of RF circuits, antennas, and high-

speed systems including design and electrical modeling, optimization, prototype development and testing. He has also 6 years' experience in microelectronic industry focusing on advanced packaging development. Dr Boyu is now a Technical Consultant for ANSYS's electronics business unit in the region.

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